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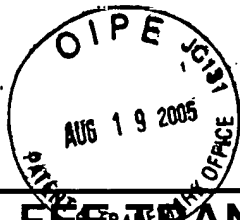
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application No.	09/981,620	
	Filing Date	October 16, 2001	
	First Named Inventor	Richard L. Coulson	
	Art Unit	2189	
	Examiner Name	K. Verbrugge	
Total Number of Pages in This Submission	11	Attorney Docket Number	42390P11456

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input checked="" type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; padding: 5px; margin-top: 10px;">31 cited references return postcard</div>
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Paul A. Mendonsa, Reg. No. 42,879 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	August 17, 2005

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FEE TRANSMITTAL for FY 2005

Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 180.00

Complete if Known

Application Number	09/981,620
Filing Date	October 16, 2001
First Named Inventor	Richard L. Coulson
Examiner Name	K. Verbrugge
Art Unit	2189
Attorney Docket No.	42390P11456

METHOD OF PAYMENT (check all that apply)

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☐ Deposit Account Deposit Account Number: 02-2666 Deposit Account Name: Blakely, Sokoloff, Taylor & Zafman LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

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FEE CALCULATION

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1251	120	2251	60	Extension for reply within first month	
1252	450	2252	225	Extension for reply within second month	
1253	1,020	2253	510	Extension for reply within third month	
1254	1,590	2254	795	Extension for reply within fourth month	
1255	2,160	2255	1,080	Extension for reply within fifth month	
1401	500	2401	250	Notice of Appeal	
1402	500	2402	250	Filing a brief in support of an appeal	
1403	1,000	2403	500	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	180.00
1809	790	1809	395	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	790	2810	395	For each additional invention to be examined (37 CFR § 1.129(b))	
Other fee (specify) _____					
SUBTOTAL (2)					(\$) 180.00

SUBMITTED BY

Complete (if applicable)

Name (Print/Type)	Paul A. Mendonsa	Registration No. (Attorney/Agent)	42,879	Telephone	(503) 439-8778
Signature		Date	08/17/05		



Docket No.: 42390P11456

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Re: Application of:

RICHARD L. COULSON

Art Group: 2189

Application No.: 09/981,620

Examiner: K. Verbrugge

Filed: October 16, 2001

For: **MASS STORAGE CACHING
PROCESSES FOR POWER REDUCTION**

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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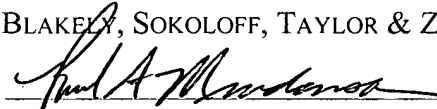
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The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

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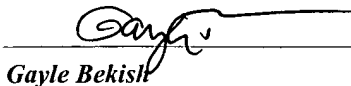


Paul A. Mendonsa, Reg. No. 42,879

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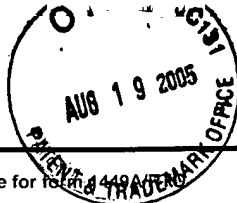
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

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of

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Application Number	09/981,620
Filing Date	October 16, 2001
First Named Inventor	Richard L. Coulson
Art Unit	2189
Examiner Name	K. Verbrugge
Attorney Docket Number	42390P11456

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-4,908,793	03-13-1990	Yamagata, et al.	
		US-4,972,364	11-20-1990	Barrett, et al	
		US-5,046,043	09-03-1991	Miller, et al	
		US-5,133,060	07-21-1992	Weber et al	
		US-5,269,019	12-07-1993	Peterson et al	
		US-5,274,799	12-28-1993	Brant et al	
		US-5,353,430	10-04-1994	Lautzenheiser	
		US-5,444,651	08-22-1995	Yamamoto et al	
		US-5,466,629	11-14-1995	Mihara et al	
		US-5,526,482	06-11-1996	Stallmo et al	
		US-5,542,066	07-30-1996	Mattson et al	
		US-5,586,291	12-17-1996	Lasker et al	
		US-5,604,881	02-18-1997	Thomas	
		US-5,615,353	03-25-1997	Lautzenheiser	
		US-5,701,516	12-23-1997	Cheng et al	
		US-5,754,888	05-19-1998	Yang et al	
		US-5,764,945	06-09-1998	Ballard	
		US-5,918,244	06-29-1999	Percival	
		US-6,025,618	02-15-2000	Chen	
		US-6,052,789	04-18-2000	Lin	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number - Kind Code ² (if known)				
		EP 0 210 384	02-24-1987	HEWLETT PACKARD		
		GB 2 210 480	06-07-1989	Sun Microsystems		
		WO 93/21579	10-28-1993	Storage Technology		
		JP 6-236241	08-23-1994	Sharp		
		WO 02/01364 A2	01-03-2002	Intel		
		WO 02/01364 A3	01-03-2002	Intel		
		WO 02/01365 A2	01-03-2002	Intel		
		WO 02/01365 A3	01-03-2002	Intel		
		WO 03/003202 A1	01-09-2003	Intel		
		WO 03/003217 A2	01-09-2003	Intel		

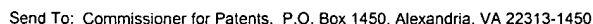
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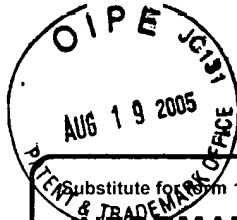
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Substitute for Form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/981,620
		Filing Date	October 16, 2001
		First Named Inventor	Richard L. Coulson
		Art Unit	2189
		Examiner Name	K. Verbrugge
		Attorney Docket Number	42390P11456
Sheet	3	of	6

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US-20030005223	01-02-2003	Coulson et al	
		US-20030046493	03-06-2003	Coulson	
		US-20030084239	05-01-2003	Stewart	
		US-20030120868	06-26-2003	Royer, Jr. et al	
		US-20030188123	10-02-2003	Royer, Jr. et al	
		US-20030188251	10-02-2003	Brown et al	
		US-20040088481	05-06-2004	Garney	
		US-20040162950	08-19-2004	Coulson	
		US-20040225826	11-11-2004	Royer, Jr. et al	
		US-20040225835	11-11-2004	Coulson	
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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code ² - Number ³ - Kind Code ⁴ (if known)				
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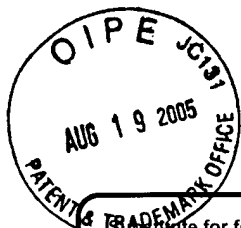
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Substitute for form 1449A/PTO				Complete if Known	
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				Art Unit	2189
				Examiner Name	K. Verbrugge
				Attorney Docket Number	42390P11456
Sheet	4	of	6		

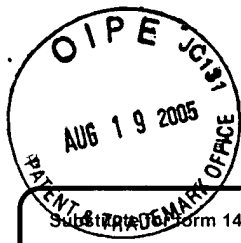
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Auciello, Orlando, et al., "The Physics of Ferroelectric Memories", Physics Today, pp. 22-27 (Jul. 1998).	
		Date, M., et al., "Opto-Ferroelectric Memories Using Vinylidene Fluoride Copolymers", IEEE, pp. 298-302 (1988).	
		Hodges, David A., et al., "Analysis and Design of Digital Integrated Circuits", McGraw-Hill, Inc., pp. 388-389 (1983).	
		Derwent Abstract, Derwent Accession No. 2000-348269, Patent Publication No. RD 431135 A, 3 pages (March 10, 2000).	
		IBM Technical Disclosure Bulletin, "Non-Volatile Cache Storage Allocation Algorithm", Vol. 38, No. 12, pp. 39-41 (Dec. 1995).	
		Lang, Sidney B., et al., "Pyroelectric Applications of Ferroelectric Polymers", IEEE, pp. 251-255 (1988).	
		Merz, W. J., et al., "Ferroelectric Storage Devices", Bell Laboratories Record, pp. 335-337, 339-342 (Sep. 1955).	

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		Attorney Docket Number	42390P11456
Sheet	5	of	4

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		Moazzami, Reza, et al., "A Ferroelectric DRAM Cell for High-Density NVRAM's", IEEE Electron Device Letters, Vol. 11, No. 10, pp. 454-456 (Oct. 1990).	
		Pulvari, C.F., "Ferroelectrics and their Memory Applications", IRE Transactions - Component Parts, pp. 3-11 (Mar. 1956).	
		Ramtron, FM1608 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-12 (Dec. 7, 1999).	
		Ramtron, FM24C64 Preliminary Datasheet, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-13 (Dec. 7, 1999).	
		Ramtron, FRAM Technology, Ramtron International Corporation, Colorado Springs, Colorado, 2 pages (Jan. 1994).	
		Ramtron, "Replacing a Dallas Semiconductor DS1225 with FRAM Memory", Application Note, Ramtron International Corporation, Colorado Springs, Colorado, pp. 1-2 (Feb. 1994).	
		Robertson, Jack, "Hyundai Enters Japanese-dominated FeRAM Market", Silicon Strategies, A Service of Semiconductor Business News, CMP Media Inc., www.siliconstrategies.com (Sep. 3, 1998).	

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NON PATENT LITERATURE DOCUMENTS

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		U.S. Patent Application No. 09/602,008, entitled "In-Line Cache", by Richard Coulson, filed June 23, 2000.	
		U.S. Patent Application No. 09/602,009, entitled "Non-Volatile Cache Expansion Board", by Richard Coulson, filed June 23, 2000.	
		U.S. Patent Application No. 09/602,010, entitled "Non-Volatile Cache", by Richard Coulson, filed June 23, 2000.	
		U.S. Patent Application No. 09/602,011, entitled "Non-Volatile Cache Integrated with Mass Storage Device", by Richard Coulson, filed June 23, 2000.	
		White, Ron, "How Computers Work", Ziff-Davis Press, pp. 71-73 (1993).	

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